

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2019-2020
II B.Tech I semester ECE Section-C

Room No : 305 North Block (NB)

w.e.f: 15-07-2019

Time(Hrs) Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
	1	2	3		4	5	6
Monday	SS	DSD		LUNCH	DSD / BS LAB		
Tuesday	DSD	EDC	NA&TL		PTSP	SS	
Wednesday	NA&TL	LIB/INT	DSD(T)		EDC / DSD LAB		
Thursday	EDC	PTSP			EDC(T)	COI	DSD
Friday	COI	PTSP	SS		SS(T)	EDC	
Saturday	BS / EDC LAB				NA&TL		COI

S.No	Subject		Faculty	Dept / Faculty
1	EDC	Electronic Devices and Circuits	S Surekha	ECE
2	NA&TL	Network Analysis and Transmission Lines	A Navila	ECE
3	DSD	Digital System Design	Nikhath Parvin	ECE
4	SS	Signals and Systems	B Eleena	ECE
5	PTSP	Probability Theory and Stochastic Processes	G Srilakshmi	ECE
6	EDC LAB	Electronic Devices and Circuits Laboratory	A Navila, S Surekha / Nazma Sultana	ECE
7	DSD LAB	Digital System Design Lab	Nikhath Parvin, Uzma Nazneen	ECE
8	BS LAB	Basic Simulation Laboratory	B Eleena, S Manjula	ECE
9	COI	Constitution of India	Y Sagarika	Management

Incharge
J Stella Mary / Shafia Tasneem

HoD
N Shribala

Principal
DrC Berin Jones