

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2019-2020
II B.Tech I semester ECE Section-B

Room No : 205 North Block (NB)

w.e.f: 15-07-2019

Time(Hrs) Day	930-1030 1	1030-1130 2	1130 - 1230 3	1230 – 1330 LUNCH	1330 - 1430 4	1430-1530 5	1530-1630 6
Monday	EDC / DSD LAB			LUNCH	PTSP	EDC	
Tuesday	PTSP		SS(T)		DSD		COI
Wednesday	EDC	PTSP	EDC(T)		COI	NA&TL	
Thursday	DSD	SS	NA&TL		BS / EDC LAB		
Friday	SS	LIB/INT	DSD		DSD / BS LAB		
Saturday	COI	EDC	NA&TL		DSD(T)	SS	

S.No	Subject	Faculty	Dept / Faculty	
1	EDC	Electronic Devices and Circuits	G Ranjitha	ECE
2	NA&TL	Network Analysis and Transmission Lines	Radhika Rayeekanti	ECE
3	DSD	Digital System Design	Nikhat Parvin	ECE
4	SS	Signals and Systems	B Eleena	ECE
5	PTSP	Probability Theory and Stochastic Processes	M Krishna Chaitanya	ECE
6	EDC LAB	Electronic Devices and Circuits Laboratory	G Ranjitha, A Navila	ECE
7	DSD LAB	Digital System Design Lab	K Amtul Salam, SVMG Phani Kumar	ECE
8	BS LAB	Basic Simulation Laboratory	M Krishna Chaitanya, Geetha / Ishrat Fatima	ECE
9	COI	Constitution of India	B Prasanna	Management

Incharge
J Stella Mary / Shafia Tasneem

HoD
N Shribala

Principal
DrC Berin Jones