

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics & Communication Engineering**  
**Time Table for the Academic Year 2019-2020**  
**II B.Tech I semester ECE Section-A**

Room No : 104 North Block (NB)

w.e.f: 15-07-2019

| Time(Hrs)<br>Day | 930-1030<br>1 | 1030-1130<br>2 | 1130 - 1230<br>3 | 1230 – 1330 | 1330 - 1430<br>4 | 1430-1530<br>5 | 1530-1630<br>6 |
|------------------|---------------|----------------|------------------|-------------|------------------|----------------|----------------|
| Monday           | DSD           |                | PTSP             | LUNCH       | COI              | EDC            |                |
| Tuesday          | NA&TL         | SS             |                  |             | EDC / DSD LAB    |                |                |
| Wednesday        | COI           | EDC            | NA&TL            |             | PTSP             |                | SS             |
| Thursday         | DSD / BS LAB  |                |                  |             | SS(T)            | DSD            | DSD(T)         |
| Friday           | BS / EDC LAB  |                |                  |             | NA&TL            |                | COI            |
| Saturday         | EDC           | LIB/INT        | DSD              |             | EDC(T)           | SS             | PTSP           |

| S.No | Subject |   | Faculty                        | Dept / Faculty |
|------|---------|---|--------------------------------|----------------|
| 1    | EDC     | Electronic Devices and Circuits             | S Surekha                      | ECE            |
| 2    | NA&TL   | Network Analysis and Transmission Lines     | A Navila                       | ECE            |
| 3    | DSD     | Digital System Design                       | Uzma Nazneem                   | ECE            |
| 4    | SS      | Signals and Systems                         | S Manjula                      | ECE            |
| 5    | PTSP    | Probability Theory and Stochastic Processes | M Krishna Chaitanya            | ECE            |
| 6    | EDC LAB | Electronic Devices and Circuits Laboratory  | S Surekha, G Ranjitha          | ECE            |
| 7    | DSD LAB | Digital System Design Lab                   | Uzma Nazneem, K Amtul Salam    | ECE            |
| 8    | BS LAB  | Basic Simulation Laboratory                 | S Manjula, M Krishna Chaitanya | ECE            |
| 9    | COI     | Constitution of India                       | V Dhanalakshmi                 | Management     |

Incharge  
J Stella Mary / Shafia Tasneem

HoD  
N Shribala

Principal  
DrC Berin Jones