

Bhoj Reddy Engineering College for Women
Department of Electronics and Communication Engineering

Time Table for the Academic Year 2017-18

II B Tech I Semester ECE-C

Room No : 304 North Block (NB)

w e f: 12 July 2017

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	SSP	M-IV	AE(T)	LUNCH	SSP(T)	NA	ET
Tuesday	EST	AE	M-IV		ET	SSP	M-IV(T)
Wednesday	ET	SSP	NA		AE	M-IV	NA(T)
Thursday	BEE / BS LAB				M-IV	ET(T)	EST
Friday	BS / EDC LAB				AE	EST	SSP
Saturday	AE	NA	ET		EDC / BEE LAB		

S No		Subject	Faculty	Dept
1	M-IV	Mathematics -IV	G Swapna	Mathematics
2	AE	Analog Eletronics	J Stella Mary	ECE
3	ET	Electical Technology	G Poorna	EEE
4	SSP	Signals and Stochastic Process	A Naga Vamshi	ECE
5	NA	Network Analysis	K Ravi Kumar	EEE
6	EST	Environmental Science and Technology	Y Swetha	Chemistry
7	EDC LAB	Electronic Devices and Circuits Lab	K Sushma , Nazma Sultana, Nikhat parvin / N Sumalatha	ECE
8	BS LAB	Basic Simulation Lab	A Naga Vamshi, G Sri Laxshmi / S Prashanthi , Bushra	ECE
9	BEE LAB	Basic Electrical Engineering Lab	G Poorna, K Ravi Kumar/ G Karunakar Reddy	EEE

Tutorial Groups according to serial numbers

G1: 1-24

G2: 25-48

G3:49-last

J Stella Mary/ Bushra
Incharge

N Shribala
HoD

Dr S Mamatha
Principal