

Bhoj Reddy Engineering College for Women
Department of Electronics and Communication Engineering
Timetable for the Academic Year 2017-18
II B Tech I Semester ECE-B

Room No : 204 North Block (NB)

w e f: 12 July 2017

Time Day	09:30-10:30	10:30-11:30	11:30 - 12:30	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	SSP	AE	ET	LUNCH	BS / EDC LAB		
Tuesday	M-IV	SSP	NA		ET	EST	AE(T)
Wednesday	EDC / BEE LAB				SSP(T)	EST	ET
Thursday	NA	ET	AE		SSP	ET(T)	M-IV
Friday	AE	M-IV	NA(T)		BEE / BS LAB		
Saturday	EST	SSP	M-IV		AE	M-IV(T)	NA

S No	Subject		Faculty	Dept
1	M-IV	Mathematics -IV	Bushra	Mathematics
2	AE	Analog Eletronics	J Stella Mary	ECE
3	ET	Electical Technology	Pramodh Singh	EEE
4	SSP	Signals and Stochastic Process	S V MG Phani Kumar	ECE
5	NA	Network Analysis	T Vinay kumar	EEE
6	EST	Environmental Science and Technology	K Sandhya	Chemistry
7	EDC LAB	Electronic Devices and Circuits Lab	Radhika Ravikrindi, Nikhat Parvin/ K Sushma , Neha Jain / Bushra	ECE
8	BS LAB	Basic Simulation Lab	S V M G Phani Kumar,B Eleena / N Sumalatha	ECE
9	BEE LAB	Basic Electrical Engineering Lab	Pramodh Singh , K Vinay Kumar	EEE

Tutorial Groups according to serial numbers

G1: 1-24

G2: 25-48

G3:49-last

J Stella Mary/ Bushra
Incharge

N Shribala
HoD

Dr S Mamatha
Principal