

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2016-17
II B.Tech I Semester ECE Section-C

Room No : 304 North Block (NB)

w.e.f: 13-06-2016

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	EDC	M-III (G1 - 304) PTSP (G2 - 303) STLD (G3 - 311)	STLD	LUNCH	EC	PTSP	SS
Tuesday	EC	SS	M-III		EC (G1 - 304) EDC (G2 - 303) SS (G3 - 311)	PTSP	STLD
Wednesday	PTSP	SS (G1 - 304) EC (G2 - 303) EDC (G3 - 311)	M-III		EDC/ BS LAB		
Thursday	STLD	EC	PTSP		EDC (G1 - 304) SS (G2 - 303) EC (G3 - 311)	EDC	M-III
Friday	BS/ EDC LAB				SS	STLD (G1 - 304) M-III (G2 - 303) PTSP (G3 - 311)	EDC
Saturday	SS	EDC	PTSP (G1 - 304) STLD (G2 - 303) M-III (G3 - 311)		STLD	M-III	EC

S.No	Subject		Faculty	Dept
1	M III	Mathematics -III	G Sangeetha	BS
2	PTSP	Probability Theory & Stochastic Processes	M Krishna Chaithanya	ECE
3	STLD	Switching Theory & Logic design	B Shobha	ECE
4	EC	Electrical Circuits	S K Vali	EEE
5	EDC	Electronic devices & circuits	Bushra	ECE
6	SS	Signals & Systems	SVMG Phani Kumar	ECE
7	EDC LAB	Electronic devices & circuits Lab	Bushra, B Shobha	ECE
8	BS LAB	Basic Simulation Lab	SVMG Phani Kumar, R Bala Bhaskar/ P A Sranvathi	ECE

Tutorial Groups :

G1: 4C1-4E4

G2: 4E5-G8

G3: 4G9- Last Number

Incharge
B Prabhavathi/S Deepika

HoD
N Shribala

Principal
Dr K Yedukondalu