

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2016-17
II B.Tech I semester ECE Section-B

Room No : 204 North Block (NB)

w.e.f: 13-06-2016

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	EDC	STLD	PTSP	LUNCH	STLD (G1 - 204) M-III (G2 - 209) PTSP (G3 - 211)	SS	EC
Tuesday	PTSP	M-III (G1 - 204) PTSP (G2 - 209) STLD (G3 - 211)	SS		EDC/ BS LAB		
Wednesday	SS	M-III	SS (G1 - 204) EC (G2 - 209) EDC (G3 - 211)		STLD	EDC	PTSP
Thursday	BS/ EDC LAB				EC	PTSP (G1 - 204) STLD (G2 - 209) M-III (G3 - 211)	M-III
Friday	EC	EDC (G1 - 204) SS (G2 - 209) EC (G3 - 211)	M-III		PTSP	STLD	EDC
Saturday	STLD	EC	EDC		EC (G1 - 204) EDC (G2 - 209) SS (G3 - 211)	M-III	SS

S.No	Subject		Faculty	Dept
1	M III	Mathematics -III	K Padma	BS
2	PTSP	Probability Theory & Stochastic Processes	M Krishna Chaithanya	ECE
3	STLD	Switching Theory & Logic design	K Anjaneyulu	ECE
4	EC	Electrical Circuits	T Vinay Kumar	EEE
5	EDC	Electronic devices & circuits	Asma Begum	ECE
6	SS	Signals & Systems	Radhika Rayeekanti	ECE
7	EDC LAB	Electronic devices & circuits Lab	Asma Begum, Anjaneyulu	ECE
8	BS LAB	Basic Simulation Lab	Radhika Rayeekanti, S Prashanthi/ G Srilaxmi	ECE

Tutorial Groups :

G1: 461-485

G2: 486-4A9

G3: 4B0-Last Number

Incharge
B Prabhavathi/S Deepika

HoD
N Shribala

Principal
Dr K Yedukondalu