

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2016-17
II B.Tech I semester ECE Section-A

Room No : 103 North Block (NB)

w.e.f: 13-06-2016

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	STLD	M-III (G1 - 103) PTSP (G2 - 201) STLD (G3 - 202)	SS	LUNCH	PTSP	M-III	EC
Tuesday	EDC/ BS LAB				PTSP (G1 - 103) STLD (G2 - 201) M-III (G3 - 202)	M-III	EDC
Wednesday	EC	EDC	PTSP		STLD	EC (G1 - 103) EDC (G2 - 201) SS (G3 - 202)	SS
Thursday	EDC	STLD	SS (G1 - 103) EC (G2 - 201) EDC (G3 - 202)		BS/ EDC LAB		
Friday	PTSP	M-III	EC		EDC (G1 - 103) SS (G2 - 201) EC (G3 - 202)	SS	STLD
Saturday	SS	STLD (G1 - 103) M-III (G2 -201) PTSP (G3 - 202)	M-III		EDC	EC	PTSP

S.No	Subject		Faculty	Dept
1	M III	Mathematics -III	K Padma	BS
2	PTSP	Probability Theory & Stochastic Processes	G Srilaxmi	ECE
3	STLD	Switching Theory & Logic design	S Suresh	ECE
4	EC	Electrical Circuits	K Vinay Kumar	EEE
5	EDC	Electronic devices & circuits	Neha Jain	ECE
6	SS	Signals & Systems	B Eleena	ECE
7	EDC LAB	Electronic devices & circuits Lab	Neha Jain, S Suresh	ECE
8	BS LAB	Basic Simulation Lab	B Eleena, G Srilaxmi	ECE

Tutorial Groups :

G1: 401-424

G2: 425-449

G3: 450-Last Number

Incharge
B Prabhavathi/S Deepika

HoD
N Shribala

Principal
Dr K Yedukondalu