

Bhoj Reddy Engineering College for Women
Department of Computer Science & Engineering
Time Table for Academic Year 2016 -17
II B.Tech I Semester CSE Section-B

Room No : 111 SB

w.e.f : 27 June 2016

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	MFCs	BEE	DS	L U N C H	DLD(G1) BEE(G2) EDC(G3)	DLD	EDC
Tuesday	DS LAB				DS(G1) MFCs(G2) P&S(G3)	DS	P&S
Wednesday	DLD	BEE	EDC		MFCs(G1) P&S(G2) DS(G3)	P&S	DS
Thursday	P&S(G1) DS(G2) MFCs(G3)	EDC	MFCs		BEE LAB		
Friday	BEE(G1) EDC(G2) DLD(G3)	DLD	P&S		MFCs	EDC	BEE
Saturday	P&S	EDC(G1) DLD(G2) BEE(G3)	DS		BEE	MFCs	DLD

S.No	Subject	Subject Name	Faculty	Dept
1	P&S	Probability and Statistics	T Suresh	BS
2	MFCs	Mathematical Foundations of Computer Science	N Kiranmai	CSE
3	DS	Data Structures	G Dayakar Reedy	CSE
4	DLD	Digital Logic Design	Farahath Sultana	CSE
5	EDC	Electronic Devices and Circuits	Nikhat Parveen	ECE
6	BEE	Basic Electrical Engineering	VSL Vani	EEE
7	EE LAB	Electrical and Electronics Lab	R Sinduja, S Manjula / VSL Vani, K Vinay Kumar, S Deepthi	ECE/EEE
8	DS LAB	Data Structures Lab	G Dayakar Reddy, V Sai Krishna, K Vasudha, C Sowmya, C Bhagyasree	CSE

Tutorial Groups (According to Roll Numbers)

G1 [111SB]: 15321A0561-15321A0584 **G2 [G03SB]:** 15321A0585-15321A05A8 **G3 [102SB]:** Remaining

Incharge

HOD

Principal