

Bhoj Reddy Engineering College for Women
Department of Computer Science & Engineering
Time Table for Academic Year 2016 -17
II B.Tech I Semester CSE Section- A

Room No : G10 SB

w.e.f : 27 June 2016

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	DS	MFCS	DLD	L U N C H	BEE	P&S	DS(G1) DLD(G2) P&S(G3)
Tuesday	EE LAB				MFCS	MFCS(G1) EDC(G2) BEE(G3)	EDC
Wednesday	BEE	DLD(G1) P&S(G2) DS(G3)	DLD		EDC	DS	P&S
Thursday	EDC	P&S	BEE(G1) MFCS(G2) EDC(G3)		DS LAB		
Friday	MFCS	EDC	BEE		P&S(G1) DS(G2) DLD(G3)	DS	DLD
Saturday	P&S	DS	EDC(G1) BEE(G2) MFCS(G3)		MFCS	DLD	BEE

S.No	Subject	Subject Name	Faculty	Dept
1	P&S	Probability and Statistics	M Sathish	BS
2	MFCS	Mathematical Foundations of Computer Science	M Jhansi Rani	CSE
3	DS	Data Structures	G Dayakar Reedy	CSE
4	DLD	Digital Logic Design	Farahath Sultana	CSE
5	EDC	Electronic Devices and Circuits	Nikhat Parveen	ECE
6	BEE	Basic Electrical Engineering	G Poorna	EEE
7	EE LAB	Electrical and Electronics Lab	Nikhat Parveen, R Sinduja / G Poorna, K.Vinay Kumar, T Vinay Kumar	ECE/EEE
8	DS LAB	Data Structures Lab	G DayakarReddy, Faiza Nasreen, M Jhansi Rani,N Kiranmai, Juveria Nousheen	CSE

Tutorial Groups (According to Roll Numbers)

G1 [G10SB]: 15321A0501-15321A0524 **G2 [G03SB]:** 15321A0525-15321A0548 **G3 [102SB]:** Remaining

Incharge

HOD

Principal