

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2016-17
III B.Tech I Semester ECE Section-B

Room No : 205 North Block (NB)

w.e.f: 13-06-2016

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	LDIC	AWP (G1 - 205) CSE (G2 - 209) CO&OS(G3 - 211)	CO&OS	LUNCH	AC/ICA&HDL Lab		
Tuesday	AC	EMI	EMI (G1 - 205) AC (G2 - 209) LDIC (G3 - 211)		CO&OS	CSE	AWP
Wednesday	CO&OS	AWP	LDIC		EMI	CO&OS(G1-205) AWP (G2 - 209) CSE (G3 - 211)	AC
Thursday	CSE	AC	AC (G1 - 205) LDIC (G2 - 209) EMI (G3 - 211)		EMI	AWP	LDIC
Friday	ICA&HDL/AC Lab				AC	LDIC (G1 - 205) EMI (G2 - 209) AC (G3 - 211)	CSE
Saturday	EMI	CSE	AWP		LDIC	CSE (G1 - 205) CO&OS(G2-209) AWP (G3 - 211)	CO&OS

S.No	Subject		Faculty	Dept
1	CSE	Control Systems Engineering	J Stella Mary	ECE
2	CO&OS	Computer Organization & Operating Systems	K Murali Krishna	CSE
3	AWP	Antennas & Wave Propagation	B Prabhavathi	ECE
4	EMI	Electronic Measurements & Instrumentation	K Shiva Prasad	ECE
5	AC	Analog Communications	C Srinivas	ECE
6	LDIC	Linear & Digital IC Applications	K Srinidhi Reddy	ECE
7	AC Lab	Analog Communications Lab	C Srinivas, K Madhu Krishna	ECE
8	ICA&HDL Lab	IC Applications & HDL Simulation Lab	K Srinidhi Reddy, K Shiva Prasad	ECE

Tutorial Groups :

G1: 461-485

G2: 486-4A9

G3: 4B0-Last Number

Incharge
B Prabhavathi/S Deepika

HoD
N Shribala

Principal
Dr K Yedukondalu