

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2016-17
III B.Tech I Semester ECE Section-A

Room No : 104 North Block (NB)

w.e.f: 13-06-2016

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	AC/ICA&HDL Lab			LUNCH	CSE	AWP (G1 - 104) CSE (G2 - 209) CO&OS(G3 - 211)	CO&OS
Tuesday	LDIC	CO&OS	AWP		AC	CO&OS(G1-104) AWP (G2 - 201) CSE (G3 - 202)	EMI
Wednesday	AWP	AC	EMI (G1 - 104) AC (G2 - 201) LDIC (G3 - 202)		ICA&HDL/AC Lab		
Thursday	CSE	AC	EMI		LDIC (G1 - 104) EMI (G2 - 201) AC (G3 - 202)	CO&OS	LDIC
Friday	EMI	CSE	AC		LDIC	AC (G1 - 104) LDIC (G2 - 201) EMI (G3 - 202)	AWP
Saturday	CO&OS	LDIC	CSE (G1 - 104) CO&OS(G2-209) AWP (G3 - 211)		AWP	EMI	CSE

S.No	Subject		Faculty	Dept
1	CSE	Control Systems Engineering	S Manjula	ECE
2	CO&OS	Computer Organization & Operating Systems	K Murali Krishna	CSE
3	AWP	Antennas & Wave Propagation	P A Sravanthi	ECE
4	EMI	Electronic Measurements & Instrumentation	N Sumalatha	ECE
5	AC	Analog Communications	Dr K Yedukondalu	ECE
6	LDIC	Linear & Digital IC Applications	S Prashanthi	ECE
7	AC Lab	Analog Communications Lab	S Manjula, P A Sravanthi	ECE
8	ICA&HDL Lab	IC Applications & HDL Simulation Lab	S Prashanthi, N Sumalatha	ECE

Tutorial Groups :

G1: 401-424

G2: 425-448

G3: 449-Last Number

Incharge
B Prabhavathi/S Deepika

HoD
N Shribala

Principal
Dr K Yedukondalu