

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics & Communication Engineering**  
**Time Table for the Academic Year 2017-18**  
**II B.Tech II Semester ECE Section-C**

Room No : 304 North Block (NB)

w.e.f : 12-02-2018

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	BEFA	AC	PDC	LUNCH	AC/PDC LAB		
Tuesday	STLD	CS	BEFA		GS LAB		
Wednesday	CS	STLD(T)	AC		STLD	CS(T)	PDC
Thursday	AE/AC LAB				PDC	AC	BEFA
Friday	PDC	STLD	CS		PDC/AE LAB		
Saturday	AC	PDC	STLD		CS	BEFA	AC

S.No	Subject		Faculty	Dept
1	STLD	Switching Theory and Logic Design	Nikhath Parvin	ECE
2	PDC	Pulse and Digital Circuits	A Navila	ECE
3	CS	Control Systems	Radhika Ravikrindi	ECE
4	AC	Analog Communication	Fariya Begum	ECE
5	BEFA	Business Economics and Financial Analysis	V Dhanalaxmi	BA
6	AC LAB	Analog Communication Lab	Fariya Begum,N Rithi Shree/ Radhika Ravikrindi	ECE
7	PDC LAB	Pulse and Digital Circuits Lab	B Jyothsna,A Navila	ECE
8	AE LAB	Analog Electronics Lab	Nikhath Parvin,G Srilakshmi/ N Sumalatha	ECE
9	GS LAB	Gender Sensitization Lab	Meena Kumari	ENGLISH

Tutorial Groups :According to serial numbers

G1: 1-26

G2 : 27-50

G3: 51- Last Number