

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics & Communication Engineering**  
**Time Table for the Academic Year 2017-18**  
**II B.Tech II semester ECE Section-B**

Room No : 204 North Block (NB)

w.e.f : 12-02-2018

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	BEFA	STLD	AC	LUNCH	STLD(T)	CS	PDC
Tuesday	AC	PDC	AC		AE/AC LAB		
Wednesday	PDC/AE LAB				PDC	AC	BEFA
Thursday	STLD	CS	BEFA		AC/PDC LAB		
Friday	PDC	CS	CS(T)		STLD	BEFA	AC
Saturday	CS	STLD	PDC		GS LAB		

S.No			Faculty	Dept
1	STLD	Switching Theory and Logic Design	K Sushma	ECE
2	PDC	Pulse and Digital Circuits	N Sumalatha	ECE
3	CS	Control Systems	P A Sravanthi	ECE
4	AC	Analog Communication	Fariya Begum	ECE
5	BEFA	Business Economics and Financial Analysis	Shugufta Nishat	BA
6	AC LAB	Analog Communication Lab	P A Sravanthi,S Manjula/Uzma Nazneen	ECE
7	PDC LAB	Pulse and Digital Circuits Lab	N Sumalatha,B Jyothsna/Radhika Ravikrindi	ECE
8	AE LAB	Analog Electronics Lab	J Stella Mary,N Rithi Shree	ECE
9	GS LAB	Gender Sensitization Lab	B Narjis Fatima	ENGLISH

Tutorial Groups :According to serial numbers

G1: 1-26

G2 : 27-50

G3: 51- Last Number