

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2017-18
II B.Tech II semester ECE Section-A

Room No : 103 North Block (NB)

w.e.f : 12-02-2018

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	GS LAB			LUNCH	CS	AC	BEFA
Tuesday	PDC/AE LAB				PDC	BEFA	CS
Wednesday	STLD	PDC	CS		AE/AC LAB		
Thursday	PDC	AC	STLD		BEFA	STLD(T)	PDC
Friday	CS	BEFA	AC		CS(T)	AC	STLD
Saturday	AC/PDC LAB				STLD	PDC	AC

S.No	Subject		Faculty	Dept
1	STLD	Switching Theory and Logic Design	K Sushma	ECE
2	PDC	Pulse and Digital Circuits	A Navila	ECE
3	CS	Control Systems	Radhika Ravikrindi	ECE
4	AC	Analog Communication	S Manjula	ECE
5	BEFA	Business Economics and Financial Analysis	V Dhanalaxmi	BA
6	AC LAB	Analog Communication Lab	S Manjula,P A Sravanthi / Radhika Ravikrindi	ECE
7	PDC LAB	Pulse and Digital Circuits Lab	T Geetha,A Navila /B Eleena	ECE
8	AE LAB	Analog Electronics Lab	K Madhu Krishna,K Sushma	ECE
9	GS LAB	Gender Sensitization Lab	Meena Kumari	ENGLISH

Tutorial Groups :According to serial numbers

G1: 1-26

G2 : 27-50

G3: 51- Last Number