

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics & Communication Engineering**  
**Time Table for the Academic Year 2017-18**

**III B.Tech II Semester ECE Section-C**

Room No : 305 North Block (NB)				w.e.f : 12-02-2018			
Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	IPR	VLSI	DSP(G1) VLSI(G2) IPR(G3)	LUNCH	MEFA	MPMC	DSP
Tuesday	VLSI	DC	MEFA		MPMC/DSP LAB		
Wednesday	MPMC	DC(G1) IPR(G2)    DSP(G3)	IPR		MEFA	VLSI	DC
Thursday	MPMC	DSP	IPR		DC	MPMC(G1) DC(G2) VLSI(G3)	VLSI
Friday	DSP/MPMC LAB				IPR(G1) DSP(G2) MPMC(G3)	DSP	IPR
Saturday	DC	MPMC	DSP		DSP	VLSI(G1) MPMC(G2) DC(G3)	MEFA

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economicis and Financial Analysis	Shugufta Nishat	BA
2	IPR	Intellectual Property Rights	T Geetha	ECE
3	DC	Digital Communications	G Srilakshmi	ECE
4	VLSI	VLSI Design	Archana Subhash	ECE
5	MPMC	Microprocessors and Microcontrollers	S Deepika	ECE
6	DSP	Digital Signal processing	S Surekha	ECE
7	MPMC LAB	Microprocessors and Microcontrollers Lab	Md Toufeeq Ahmed, K Shrinidhi Reddy/ K Sushma	ECE
8	DSP LAB	Digital Signal processing Lab	S Surekha, Shafia Tasneem/B Eleena	ECE

Tutorial Groups :According to serial numbers

G1: 1-26

G2 : 27-50

G3: 51- Last Number