

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2017-18
III B.Tech II Semester ECE Section-B

Room No : 205 North Block (NB)

w.e.f : 12-02-2018

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	MPMC	IPR	MPMC	LUNCH	VLSI(G1) DC(G2) DSP(G3)	DC	MEFA
Tuesday	DC	DSP	VLSI		IPR	IPR(G1) MPMC(G2) DC(G3)	MPMC
Wednesday	MPMC/DSP LAB				VLSI	DSP	IPR
Thursday	DSP	DC	MPMC(G1) VLSI(G2) IPR(G3)		IPR	MEFA	VLSI
Friday	MEFA	DSP			DC(G1) DSP(G2) MPMC(G3)	DSP	DC
Saturday	VLSI	DSP(G1) IPR(G2) VLSI(G3)	MEFA		DSP/MPMC LAB		

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics and Financial Analysis	Shugufta Nishat	BA
2	IPR	Intellectual Property Rights	G Poorna	EEE
3	DC	Digital Communications	Nazma Sulatna	ECE
4	VLSI	VLSI Design	S Surekha	ECE
5	MPMC	Microprocessors and Microcontrollers	Uzma Nazneen	ECE
6	DSP	Digital Signal processing	Ishrat Fatima	ECE
7	MPMC LAB	Microprocessors and Microcontrollers Lab	Shafia Tasneem,K Madhu Krishna/Uzma Nazneen	ECE
8	DSP LAB	Digital Signal processing Lab	Ishrat Fatima,K Srinidhi Reddy/Nazma Sultana	ECE

Tutorial Groups :According to serial numbers

G1: 1-26

G2 : 27-50

G3: 51- Last Number