

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2017-18
III B.Tech II Semester ECE Section-A

Room No : 104 North Block (NB)

w.e.f : 12-02-2018

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	MPMC/DSP LAB			LUNCH	VLSI	DSP	IPR
Tuesday	MPMC	MEFA	VLSI		MPMC(G1) DSP(G2) DC(G3)	IPR	DC
Wednesday	DSP	DSP	MEFA		DC	DC(G1) IPR(G2) MPMC(G3)	MPMC
Thursday	IPR	VLSI(G1) MPMC(G2) DSP(G3)	VLSI		DSP/MPMC LAB		
Friday	VLSI	DC	IPR(G1) DC(G2) VLSI(G3)		MEFA	MPMC	DSP
Saturday	MEFA	DC	MPMC		DSP	IPR	DSP(G1) VLSI(G2) IPR(G3)

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics and Financial Analysis	Shugufta Nishat	BA
2	IPR	Intellectual Property Rights	T Geetha	ECE
3	DC	Digital Communications	Nazma Sultana	ECE
4	VLSI	VLSI Design	Archana Subhash	ECE
5	MPMC	Microprocessors and Microcontrollers	Uzma Nazneen	ECE
6	DSP	Digital Signal processing	B Eleena	ECE
7	MPMC LAB	Microprocessors and Microcontrollers Lab	K Srinidhi Reddy, J Stella Mary/Shafia Tasneem	ECE
8	DSP LAB	Digital Signal processing Lab	B Eleena, J Madhavan/Ishrat Fatima	ECE

Tutorial Groups :According to serial numbers

G1: 1-26

G2 : 27-50

G3: 51- Last Number