

Bhoj Reddy Engineering College for Women
Department of Electrical & Electronics Engineering
Time Table for II B Tech II Semester, Academic Year 2016 -17

Room No : G06 North Block (NB)

w e f : 09 December 2016

Time(Hrs)	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
Day	1	2	3		4	5	6
Monday	MEFA	NT	EC	L U N C H	EM-I LAB/ ECS LAB		
Tuesday	PS-I	EM-II	MEFA		GS LAB		
Wednesday	EM-I LAB/ ECS LAB				STLD	STLD	MEFA
Thursday	PS-I	STLD	NT		EC	NT(G1-G04) EC(G2-G05) STLD(G3-G11)	EM-II
Friday	NT	EC	EC(G1-G04) STLD(G2-G05) NT(G3-G11)		EM-II	MEFA	PS-I
Saturday	EC	PS-I	STLD(G1-G04) NT(G2-G05) EC(G3-G11)		STLD	EM-II	NT

S.No	Subject	Faculty	Dept
1	Managerial Economics & Financial Analysis(MEFA)	Lavanya G	MBA
2	Power Systems-I(PS-I)	Vinay Kumar T	EEE
3	Electronic Circuits(EC)	Anjaneyulu K	ECE
4	Switching Theory and Logic Design(STLD)	Manju Bhargavi R	EEE
5	Network Theory(NT)	Vali SK	EEE
6	Electrical Machines-II(EM-II)	Deepthi S	EEE
7	Electrical Machines Lab-I(EM-I LAB)	Sri Laxmi Vani V, Deepthi S	EEE
8	Electrical Circuits and Simulation Lab(ECS LAB)	Karunakar Reddy G, Vali SK	EEE
9	Gender Sensitization Laboratory(GS LAB)	Padmaja B, Jyothsna S	

Groups (According to serial numbers):

G1: 1-24

G2: 25-47

G3: 48-72

Vali SK
Incharge

Mastanamma Y
HoD

Dr Yedukondalu K
Principal

Bhoj Reddy Engineering College for Women
Department of Electrical and Electronics Engineering
Time Table for III B Tech II Semester, Academic Year 2016 -17

Room No : G04 North Block (NB)

w e f : 09 December 2016

Time(Hrs)	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
Day	1	2	3		4	5	6
Monday	IPR	CMPS	ES	L U N C H	SD(G1-G04) MID(G2-G05) IPR(G3-G11)	MID	SD
Tuesday	CSS LAB/ PES LAB				EI	CMPS(G1-G04) SD(G2-G05) EI(G3-G11)	CMPS
Wednesday	MID	SD	IPR		CMPS	IPR(G1-G04) EI(G2-G05) MID(G3-G11)	EI
Thursday	CMPS	EI(G1-G04) CMPS(G2-G05) SD(G3-G11)	SD		ES	EI	MID
Friday	ES	CMPS	IPR		PES LAB/CSS LAB		
Saturday	EI	SD	MID		IPR	MID(G1-G04) IPR(G2-G05) CMPS(G3-G11)	ES

S.No	Subject	Faculty	Dept
1	Electrical and Electronics Instrumentation(EEI)	Karunakar Reddy G	EEE
2	Static Drives(SD)	Mastanamma Y	EEE
3	Computer Methods in Power Systems(CMPS)	Sri Laxmi Vani V	EEE
4	Microprocessors and Interfacing Devices(MID)	Stella Mary J	ECE
5	Environmental Studies(ES)	Supriya Naik K	BS
6	Intellectual Property Rights(IPR)	Poorna G	EEE
7	Control Systems and Simulation Lab(CSS LAB)	Sri Laxmi Vani V, Mastanamma Y/ Manju Bhargavi R	EEE
8	Power Electronics and Simulation Lab(PES LAB)	Vali SK, Pramod Singh/ Poorna G	EEE

Groups (According to serial numbers):

G1: 1 - 23

G2: 24-46

G3: 47 - 70

Vali SK
Incharge

Mastanamma Y
HoD

Dr Yedukondalu K
Principal

Bhoj Reddy Engineering College for Women
Department of Electrical and Electronics Engineering
Time Table for IV B Tech II Semester, Academic Year 2016 -17

Room No : G03 North Block (NB)

w e f : 09 December 2016

Time(Hrs) Day	9:30-10:30	10:30-11:30	11:30 - 12:30	12:30 - 13:30	13:30-14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	CVV			L U N C H	PW		
Tuesday	PW				PW		
Wednesday	PW				PW		
Thursday	HVDC	NNFL	NNFL(G1-G04) ACS(G2-G05) HVDC(G3-G11)		ACS	NNFL	ACS
Friday	NNFL	ACS(G1-G03) HVDC(G2-G05) NNFL(G3-G11)	HVDC		TS		
Saturday	ACS	HVDC(G1-G03) NNFL(G2-G05) ACS(G3-G11)	HVDC		TS		

S.No	Subject	Faculty	Dept
1	Fundamentals of HVDC and FACTS Devices(HVDC)	Deepthi S	EEE
2	Neural Networks and Fuzzy Logic(NNFL)	Poorna G	EEE
3	Advanced Control Systems(ACS)	Manju Bhargavi R	EEE
4	Technical Seminar(TS)	Pramod Singh	EEE
5	Comprehensive Viva-Voce	Manju Bhargavi R	EEE
6	Project Work(PW)	Deepthi S	EEE

Groups (According to serial numbers):

G1: 1 - 24

G2: 25-48

G3:49- 73

Vali SK
Incharge

Mastanamma Y
HoD

Dr Yedukondalu K
Principal