

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**II B Tech II Semester ECE-A**

Room No : 103 North Block (NB)

w e f : 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 12:30	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	GS LAB			<b>L U N C H</b>	ES	DDV	ES
Tuesday	PEE	EMTL	DDV		PDC	PDC (G1-103) DDV (G2-201) EMTL (G3-202)	ECA
Wednesday	PDC	PEE	ECA		ECPC /ET LAB		
Thursday	ECA	EMTL (G1-103) PDC (G2-201) DDV (G3-202)	PDC		EMTL	ES	DDV
Friday	DDV	DDV (G1-103) EMTL (G2-201) PDC (G3-202)	EMTL		ECA	PDC	PEE
Saturday	ES	PEE	EMTL		ET / ECPC LAB		

S No	Subject		Faculty	Dept
1	PEE	Principles of Electrical Engineering	Vinay Kumar K	EEE
2	ECA	Electronic Circuit Analysis	Sushma K	ECE
3	PDC	Pulse and Digital Circuits	Deepika S	ECE
4	ES	Environmental Studies	Sandhya K	BS
5	EMTL	Electromagnetic Theory and Transmission Lines	Navila A	ECE
6	DDV	Digital Design through Verilog	Sravanthi P A	ECE
7	ET LAB	Electrical Technology Laboratory	Vinay Kumar K, Vinay Kumar T	EEE
8	ECPC LAB	Electronic Circuits and Pulse Circuits Laboratory	Deepika S, Sushma K	ECE
9	GS LAB	Gender Sensitization Laboratory	Padmaja B, Jyothsna S	ENGLISH

Tutorial Groups according to serial numbers

G1: 1-22

G2: 23-44

G3: 45-66

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**II B Tech II Semester ECE-B**

Room No : 204 North Block (NB)

w e f: 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	PEE	DDV	ES	<b>L U N C H</b>	PDC (G1-204) DDV (G2-209) EMTL (G3-211)	EMTL	PDC
Tuesday	DDV	EMTL	PDC		DDV (G1-204) EMTL (G2-209) PDC (G3-211)	PEE	ECA
Wednesday	GS LAB				ES	ECA	DDV
Thursday	EMTL	ECA	PEE		ECPC / ET LAB		
Friday	ET / ECPC LAB				PDC	PEE	EMTL
Saturday	PDC	ECA	ES		DDV	EMTL (G1-204) PDC (G2-209) DDV (G3-211)	ES

S No	Subject		Faculty	Dept
1	PEE	Principles of Electrical Engineering	Vinay Kumar T	EEE
2	ECA	Electronic Circuit Analysis	Neha Jain	ECE
3	PDC	Pulse and Digital Circuits	Sumalatha N	ECE
4	ES	Environmental Studies	Sandhya K	BS
5	EMTL	Electromagnetic Theory and Transmission Lines	Prashanthi S	ECE
6	DDV	Digital Design through Verilog	Sravanthi P A	ECE
7	ET LAB	Electrical Technology Laboratory	Vinay Kumar T, Vinay Kumar K	EEE
8	ECPC LAB	Electronic Circuits and Pulse Circuits Laboratory	Sumalatha N, Neha Jain	ECE
9	GS LAB	Gender Sensitization Laboratory	Jyothisna Devi S, Narjis Fathima	ENGLISH

Tutorial Groups according to serial numbers

G1: 1-24

G2: 25-48

G3: 49-71

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**II B Tech II Semester ECE-C**

Room No : 304 North Block (NB)

w e f: 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	ECA	ES	EMTL	<b>L U N C H</b>	GS LAB		
Tuesday	DDV	PDC	EMTL (G1-304) PDC (G2-303) DDV (G3-311)		ECPC / ET LAB		
Wednesday	PDC	DDV	PEE		DDV (G1-304) EMTL (G2-303) PDC (G3-311)	EMTL	ECA
Thursday	ET / ECPC LAB				PEE	ECA	ES
Friday	PEE	ECA	PDC		EMTL	ES	DDV
Saturday	EMTL	DDV	PEE		PDC	ES	PDC(G1-304) DDV (G2-303) EMTL (G3-311)

S No	Subject	Faculty	Dept
1	PEE	Pramod Singh	EEE
2	ECA	Suresh S	ECE
3	PDC	Radhika Ravikrindi	ECE
4	ES	Supriya Nayak	BS
5	EMTL	Prashanthi S	ECE
6	DDV	Manjula S	ECE
7	ET LAB	Pramod Singh, Vinay Kumar K	EEE
8	ECPC LAB	Radhika Ravikrindi, Suresh S	ECE
9	GS LAB	Narjis Fathima, Grace Rathna D	ENGLISH

Tutorial Groups according to serial numbers

G1: 1-23

G2: 24-46

G3: 47-69

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**III B Tech II Semester ECE-A**

Room No : 104 North Block (NB)

w e f: 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 12:30	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	IPR	MPMC	MEFA	<b>L U N C H</b>	DC	VLSI (G1-104) MPMC (G2 -201) DSP (G3-202)	DSP
Tuesday	MPMC / DSP LAB				DSP (G1-104) IPR (G2-201) DC (G3-202)	MEFA	DC
Wednesday	MEFA	VLSI	MPMC		DSP / MPMC LAB		
Thursday	VLSI	DSP	DC		DC (G1-104) VLSI (G2-201) MPMC (G3-202)	DSP	IPR
Friday	DC	IPR	MPMC (G1-104) DSP (G2 -201) IPR (G3-202)		DSP	MPMC	VLSI
Saturday	DSP	IPR (G1-104) DC (G2-201) VLSI (G3-202)	VLSI		MEFA	IPR	MPMC

S.No	Subject	Faculty	Dept
1	MEFA	Managerial Economics and Financial Analysis Sowmya E	ENGLISH
2	IPR	Intellectual Property Rights Saba sultana	ECE
3	DC	Digital Communications Srilakshmi G	ECE
4	VLSI	VLSI Design Surekha S	ECE
5	MPMC	Microprocessors and Microcontrollers Krishna Chaitanya M	ECE
6	DSP	Digital Signal Processing Eleena B	ECE
7	MPMC LAB	Microprocessors and Microcontrollers Laboratory Krishna Chaitanya M, SVMG Phani Kumar C	ECE
8	DSP LAB	Digital Signal Processing Laboratory Madhu Krishna K, Srinidhi Reddy K/ Suma Latha N	ECE

Tutorial Groups according to serial numbers

G1: 1-23

G2:24-46

G3:47-68

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**III B Tech II Semester ECE-B**

Room No : 205 North Block (NB)

w e f : 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	MEFA	DC	DC (G1-205) VLSI (G2-209) MPMC (G3 -211)	<b>L U N C H</b>	MPMC / DSP LAB		
Tuesday	IPR	VLSI	MEFA		MPMC	VLSI (G1-205) MPMC (G2 -209) DSP (G3-211)	DSP
Wednesday	DC	IPR	MPMC (G1-205) DSP (G2-209) IPR (G3- 211)		DSP	MEFA	VLSI
Thursday	DSP / MPMC LAB				MPMC	IPR (G1-205) DC (G2- 209) VLSI (G3-211)	DC
Friday	MEFA	DSP	MPMC		VLSI	DSP (G1-205) IPR (G2-209) DC (G3-211)	IPR
Saturday	VLSI	MPMC	DSP		IPR	DC	DSP

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics and Financial Analysis	Sowmya E	ENGLISH
2	IPR	Intellectual Property Rights	Bushra	ECE
3	DC	Digital Communications	Srilakshmi G	ECE
4	VLSI	VLSI Design	Surekha S	ECE
5	MPMC	Microprocessors and Microcontrollers	Kiranmayi B	ECE
6	DSP	Digital Signal Processing	Eleena B	ECE
7	MPMC LAB	Microprocessors and Microcontrollers Laboratory	Kiranmayi B, Navila A / Krishna Chaitanya	ECE
8	DSP LAB	Digital Signal Processing Laboratory	Bushra, Radhika Rayeekanti / Anjaneyulu	ECE

Tutorial Groups according to serial numbers

G1: 1-24

G2:24-47

G3:48-70

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**III B Tech II Semester ECE-C**

Room No : 305 North Block (NB)

w e f : 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	VLSI	MPMC	IPR	<b>L U N C H</b>	MEFA	IPR (G1-305) DC (G2- 303) VLSI (G3 -311)	DC
Tuesday	MEFA	DC	VLSI		DC(G1-305) VLSI (G2 -303) MPMC (G3 -311)	IPR	MPMC
Wednesday	MPMC / DSP LAB				MEFA	DSP	IPR
Thursday	DC	MPMC(G1-305) DSP (G2-303) IPR (G3-311)	IPR		DSP	VLSI	DSP
Friday	MPMC	DSP	VLSI(G1-305) MPMC (G2-303) DSP (G3-311)		DSP / MPMC LAB		
Saturday	DSP	MPMC	VLSI		DSP(G1-305) IPR (G2-303) DC (G3-311)	DC	MEFA

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics and Financial Analysis	Sowmya E	ENGLISH
2	IPR	Intellectual Property Rights	Saba sultana	ECE
3	DC	Digital Communications	Jyothsna B	ECE
4	VLSI	VLSI Design	Amtul Salam K	ECE
5	MPMC	Microprocessors and Microcontrollers	Nikhath Parvin	ECE
6	DSP	Digital Signal Processing	Manjula S	ECE
7	MPMC LAB	Microprocessors and Microcontrollers Laboratory	Nikhath Parvin, Navila A	ECE
8	DSP LAB	Digital Signal Processing Laboratory	Srinidhi Reddy K, Amtul Salam K / Swapna M	ECE

Tutorial Groups according to serial numbers

G1: 1-24

G2:24-47

G3:48-70

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**IV B Tech II Semester ECE-A**

Room No : 106 North Block (NB)

w e f: 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	CVV			L U N C H	PROJECT WORK		
Tuesday	PROJECT WORK				PROJECT WORK		
Wednesday	PROJECT WORK				PROJECT WORK		
Thursday	SC	WCN	RS		SC	RS (G1-106) WCN (G2-201) SC (G3-202)	WCN
Friday	WCN (G1-106) SC (G2-201) RS(G3-202)	RS	SC		SEMINAR		
Saturday	WCN	RS	SC (G1-106) RS (G2-201) WCN (G3-202)		SEMINAR		

S.No	Subject		Faculty	Dept
1	SC	Satellite Communications	Madhu Krishna K	ECE
2	RS	Radar Systems	Jyothsna B	ECE
3	WCN	Wireless Communications and Networks	Srinidhi Reddy K	ECE
4	SEM	Seminar	Anjaneyulu K, Neha Jain, Radhika Ravikrindi/Madhu Krishna K	ECE
5	CVV	Comprehensive Viva-voce	Jyothsna B, Manjula S, Stella Mary J	ECE

Tutorial Groups according to serial numbers

G1: 1-23

G2: 24-46

G3:47-69

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**IV B Tech II Semester ECE-B**

Room No : 210 North Block (NB)

w e f: 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	PROJECT WORK			L U N C H	PROJECT WORK		
Tuesday	PROJECT WORK				PROJECT WORK		
Wednesday	CVV				PROJECT WORK		
Thursday	RS	SC	WCN(G1-210) SC(G2-209) RS(G3-211)		SEMINAR		
Friday	WCN	RS	SC		RS (G1-210) WCN (G2-209) SC (G3-211)	WCN	SC
Saturday	RS	SC (G1-210) RS (G2-209) WCN (G3-211)	WCN		SEMINAR		

S.No	Subject		Faculty	Dept
1	SC	Satellite Communications	Dr Yedukondalu K	ECE
2	RS	Radar Systems	Stella Mary J	ECE
3	WCN	Wireless Communications and Networks	Radhika Rayeekanti	ECE
4	SEM	Seminar	Radhika Rayeekanti, Nikhat Parvin/ Swapna M, Sushma K/ Kiranmayi B	ECE
5	CVV	Comprehensive Viva-voce	Shribala N, Sravanthi P A, Prashanthi S	ECE

Tutorial Groups according to serial numbers

G1: 1-22

G2: 23-44

G3:45-65

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal



**Bhoj Reddy Engineering College for Women**  
**Department of Electronics and Communication Engineering**  
**Time Table for the Academic Year 2016-17**  
**IV B Tech II Semester ECE-C**

Room No : 310 North Block (NB)

w e f: 9 December 2016

Time Day	09:30-10:30	10:30-11:30	11:30 - 1230	12:30 – 13:30	13:30 - 14:30	14:30-15:30	15:30-16:30
	1	2	3		4	5	6
Monday	PROJECT WORK			L U N C H	PROJECT WORK		
Tuesday	CVV				PROJECT WORK		
Wednesday	PROJECT WORK				PROJECT WORK		
Thursday	WCN	SC	RS		SC	RS (G1-310) WCN (G2-303) SC (G3-311)	WCN
Friday	SC	WCN (G1-310) SC (G2-303) RS (G3-311)	RS		SEMINAR		
Saturday	WCN	RS	SC (G1-310) RS (G2-303) WCN (G3-311)		SEMINAR		

S.No	Subject		Faculty	Dept
1	SC	Satellite Communications	Swapna M	ECE
2	RS	Radar Systems	Shribala N	ECE
3	WCN	Wireless Communications and Networks	SVMG Phani Kumar C	ECE
4	SEM	Seminar	Amtul Salam K, Suresh S, Saba sultana / Surekha S	ECE
5	CVV	Comprehensive Viva-voce	Shribala N, Srilakshmi G, Deepika S	ECE

Tutorial Groups according to serial numbers

G1: 1-24

G2: 25-48

G3:49-71

Deepika S/Stella Mary J  
Incharge

Shribala N  
HoD

Dr Yedukondalu K  
Principal